

Long-Term Storage of the Solar Arrays for the Defense Meteorological Satellite Program (DMSP) 5D3 Spacecraft

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This technical report has been reviewed and is approved for publication. Publication of this report does not constitute Air Force approval of the report's findings or conclusions. It is published only for the exchange and stimulation of ideas.


Lt. Col. John Varljen
SMC/WXS

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14. ABSTRACT Lockheed Martin built the United States Air Force's DMSP 5D-3 Spacecraft. The satellite was designed for a mission that consists of two years of ground storage and 45 days of prelaunch checkout, followed by 3-1/2 years of operational life. The 5D-3 spacecraft will operate in a circular, sun-synchronous orbit with an inclination of 98.7° at an altitude of 458 nmi. The build of five 5D-3 spacecraft was completed in 1997. The first 5D-3 satellite, designated F-16, was launched on October 18, 2003 after six years of storage. The next 5D-3 DMSP, designated F-17, is scheduled to be launched in 2006. The remaining three satellites will be launched roughly every two years to maintain the two-satellite DMSP constellation. The final DMSP satellite may not be launched until 2012, after 16 years of storage! Thus, the Air Force is concerned about the age of the DMSP spacecraft hardware and possible age-related degradation of the hardware. This report examines how well the solar arrays for the 5D-3 spacecraft withstood the extended storage at the Lockheed Martin plant located in Sunnyvale California.				
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1. Description of the 5D-3 Solar Arrays

The solar array includes ten flat structurally identical solar cell panels. The ten panels are assembled into two five packs that are attached to the solar array boom. When deployed, the two five packs are positioned into a single plane symmetrical around the solar-array boom, as shown in Figure 1.

The solar-array boom can be canted with respect to the +Z axis of the spacecraft from 12° to 42° in 5° increments. The cant is set prior to launch and cannot be changed once the spacecraft is on orbit. Thus, the Solar Array for the DMSP 5D-3 Spacecraft is a single-axis tracking system. The single axis of tracking is accomplished by rotating the solar array around the boom once per orbit. The rotation of the solar array is such that the solar array is essentially motionless in inertial space. Figure 2 is an artist's rendition of the DMSP satellite on orbit. Figure 3 provides the peak solar array capacity as a function of sun angle with optimal cant angle for the each sun angle for the month of June after five years on orbit. Figure 4 is for the month of December after five years on orbit.



Figure 1. Photo of deployed solar array for DMSP S-17 spacecraft.

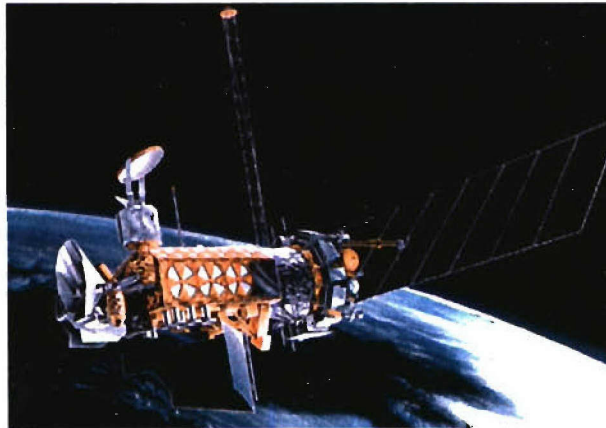


Figure 2. Artist's rendition of DMSP 5D-3 spacecraft on orbit with deployed solar array.

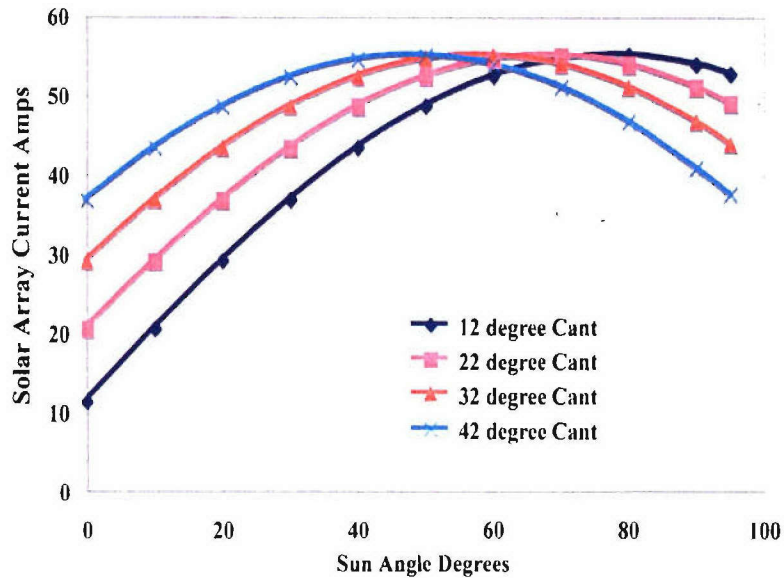


Figure 3. DMSP 16-20 peak solar array capacity in June at the end of five years on orbit.

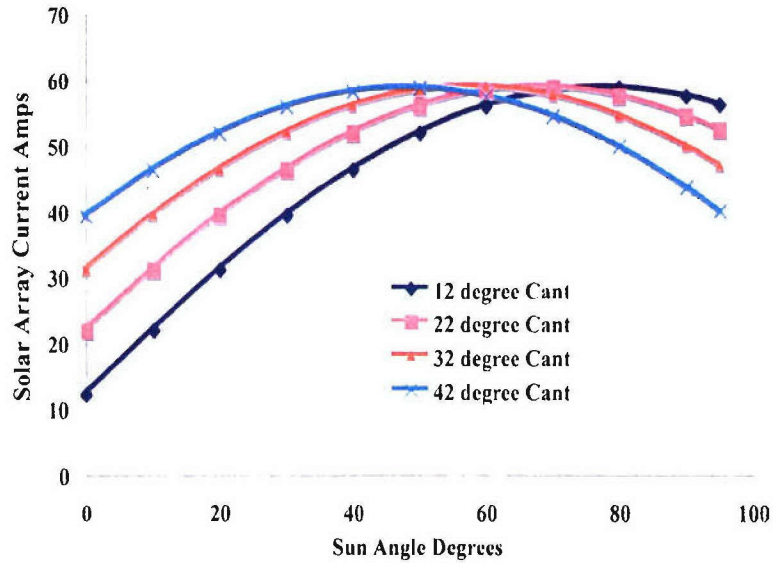


Figure 4. DMSP 16-20 peak solar array capacity in December to the end of five years on orbit.

The phasing of the solar array normally is such that the plane of the array, neglecting the cant angle, is normal to the sun. However, the array can be oriented at an offset angle (with respect to normal tracking of the sun) with a ground command. This feature has been used in the past to allow the solar array to provide shading of the batteries from the sun on Flight F-16. This was desired at low sun angles where the vehicle is in full sun.

Each of the ten solar array panels has seven circuits, which consist of three cells in parallel with 89 or 90 cells in series. The cells are 4.04- x 2.02-cm high-efficiency N-on-P silicon with backside reflec-

beginning of life power rating of 2207 W. Each of the ten panels is 107.5 by 24.3 in. and weighs 19.9 lb. Each panel has seven shunt circuits (one for each solar array circuit) and is configured as shown in Figure 5.

The wiring harness of the solar array has individual wires, positive, negative and tap points, for each solar array circuit, which are brought out to the harness connector for a Five Pack. Thus, it is possible to obtain the Current-Voltage (I-V) Characteristic for each circuit of three cells in parallel by 89 or 90 cells in series. In addition, the I-V characteristic of the 32 series cells (CKT A) and 58 or 58 series cells (CKT B) can be obtained.

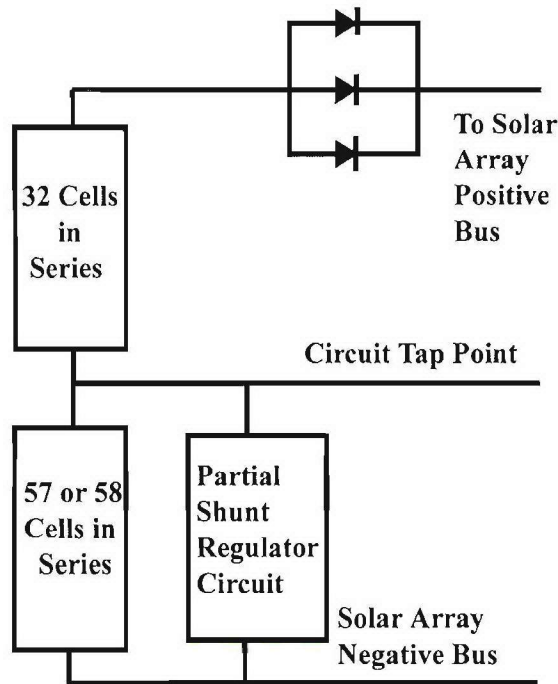


Figure 5. Sample schematic for each of 70 solar array circuits.

2. Results of I-V Testing of the DMSP S-17 Solar Array

The I-V curves were obtained for each circuit and subcircuit for the DMSP S-17 spacecraft when the arrays completed the manufacturing process in 1997 after rework to correct a shorting of the shunt circuit wiring in 1999 and prior to installation on the spacecraft during launch processing operations in 2005. Table 1 provides a summary of the results of these tests for a representative solar cell circuit. The data presented in Table 1 includes the Voltage Open Circuit (VOC), Current Short Circuit (ISC), Maximum Power Point (Pmax), Voltage at Maximum Power Point (Vmax), Current at Maximum Power Point (Imax), Fill Factor (FF), and temperature at which the data was taken.

Figures 6, 7, and 8 provide the actual I-V characteristics for Circuit 4 Panel A1, Circuit 4A Panel A1, and Circuit 4B Panel A1.

Table 1. Summary of I-V Characteristic Test Results Summary

Circuit 4 Panel A1							
Date	VOC	ISC	Pmax Watts	Vmax	Imax	FF	Temp. C
8/22/97	53.14	0.9692	40.48	44.39	0.9118	0.786	28
1/14/99	52.94	0.986	40.93	44.16	0.9268	0.784	28
1/28/05	53.2	0.9995	41.32	44.4	0.931	0.777	28
Circuit 4A Panel A1							
8/22/97	18.91	0.9802	14.49	15.73	0.9215	0.782	28
1/14/99	18.9	0.9832	14.5	15.7	0.9237	0.78	28
1/28/05	18.87	1.001	14.73	15.7	0.938	0.779	28
Circuit 4B Panel A1							
8/22/97	34.36	0.9753	26.3	28.66	0.9176	0.785	28
1/14/99	34.22	0.9779	26.37	28.53	0.9241	0.788	28
1/28/05	34.25	0.989	26.53	28.57	0.929	0.783	28

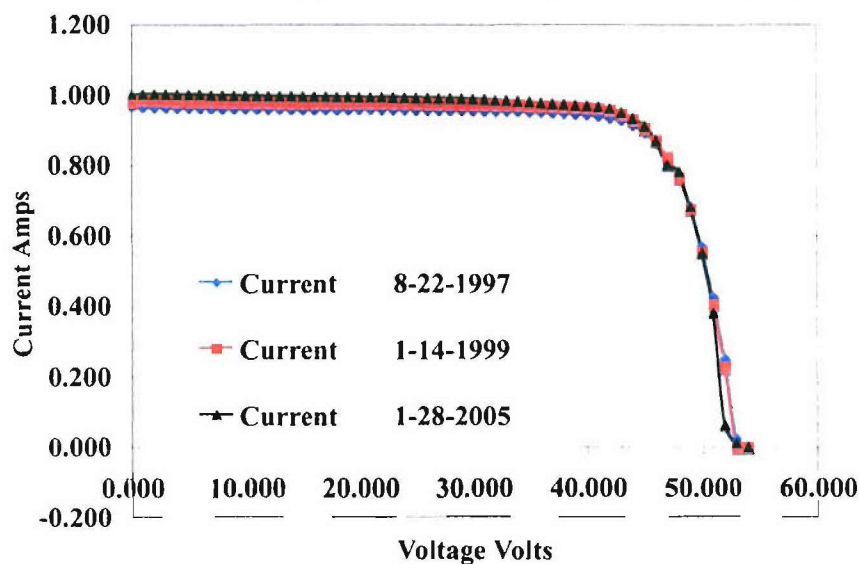


Figure 6. I-V Curves for Circuit 4 Panel A1.

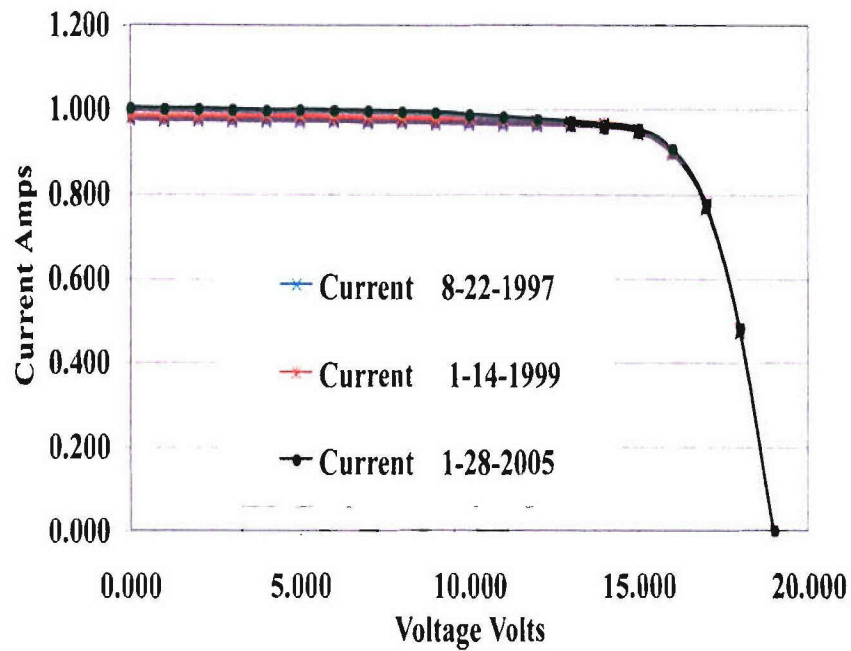


Figure 7. I-V Curve for Circuit 4A Panel A1.

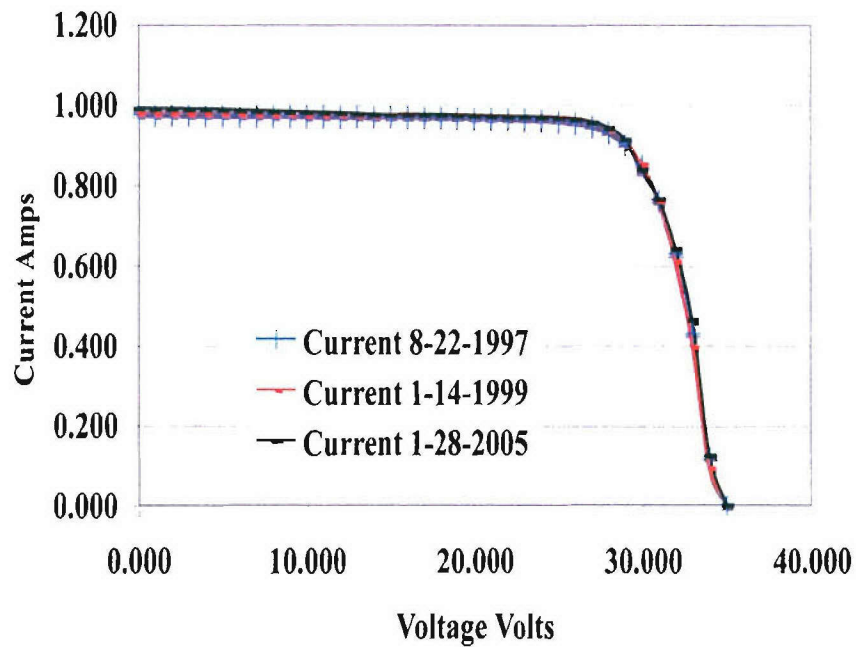


Figure 8. I-V Curve for Circuit 4B Panel A1.

3. Conclusions

The tests performed to date on the DMSP S-17 solar array do not show any degradation over the period of time that the array was in storage. A method has been demonstrated whereby I-V curves of the individual 70 circuits taken over the period of time in storage are compared to determine the health of the solar arrays after extended storage periods.

LABORATORY OPERATIONS

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